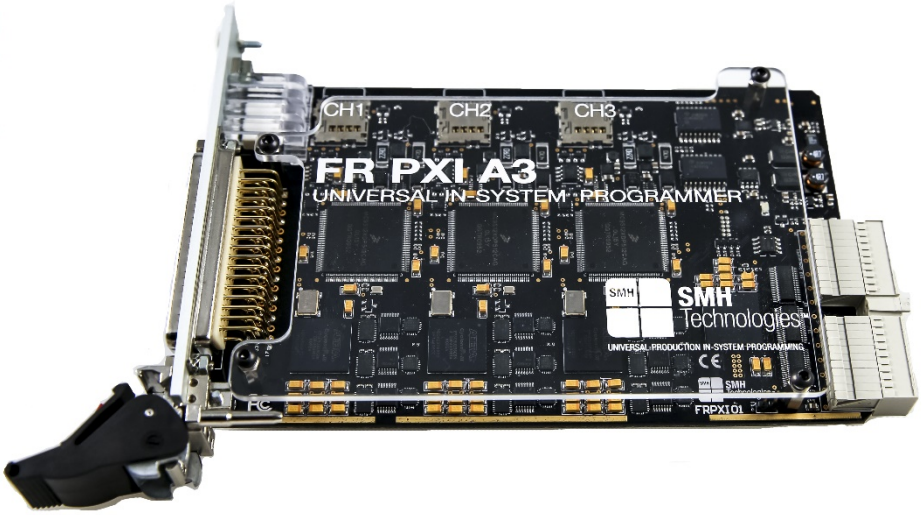
FlashRunner
FRPXIA3
In-System Programmer



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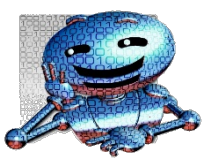


Fast and secure

Overview

FlashRunner FRPXIA3 is the brand new PXI module, for Gang In System Programming.

FlashRunner PXI is based on FlashRunner technology, the fast and reliable programming system for Flash-based microcontroller and serial memories.

Features

- First in the world programming solution for PXI system
- Fully hardware and software ATE integration
- Multi-target parallel programming channels
- Powered by Flashrunner technology, the fastest and most reliable programming system.

Why PXI?

Engineers around the world have made the switch to PXI to realize faster test execution times, improved software development productivity, faster throughoutput, and increased scalability, which dramatically reduce their overall system costs.

Now, for the very first time, they have the opportunity to integrate the In-System Programming (ISP) function in their PXI systems.

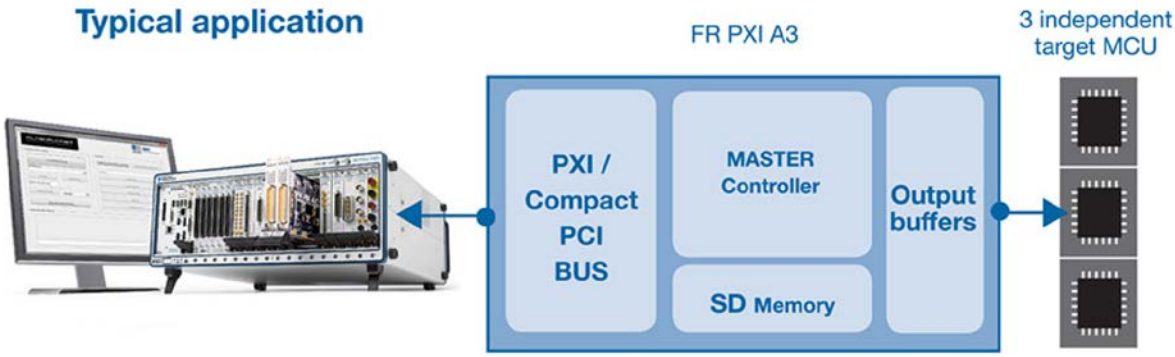
FRPXIA3 is now the ideal solution for the Automatic Test Equipment (ATE) in production environment.

This is Flashrunner FRPXIA3

- CompactPCI 2.0 compliant, PXI fully compatible
- Integrated in only one slot of your PXI chassis
- 3 parallel in system programming channels. For each channel:
  - Five digital I/O lines
  - Two digital I/O or analog output lines
  - Two programmable output voltages
  - One programmable clock output
- Fast programming algorithms developed to reach the memory technology speed limit of the target device
- Supports most ISP protocols (BDM, JTAG, SPI, I2C, MON, ICC, SCI, etc.);
- Flexible, fully configurable
- Designed for performance and reliability

FlashRunner FRPXIA3 Software Features

- DLL, LabVIEW libraries and examples for easy and fast software integration
- ASCII-Based commands
- Optional Data Protection System to make the contents
- of the binary file to be programmed to the target device
- not readable (and not duplicable) by non-authorized people
- Log files • Erase, blank check, program, read, verify, oscillator trimming, etc.
- FlashRunner's open architecture makes its firmware
- easily upgradable to support both new devices and new features

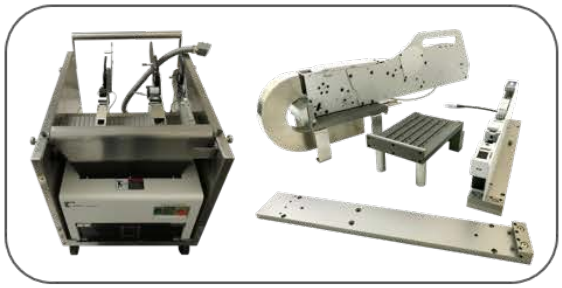


Programming sites	3 independent, parallel	**Dimensions**	w/o front panel 160 x 100 mm
Protocols	UART, JTAG, SPI, I2C,BDM, SWD etc...	**Front Panel Dimension:**	128 x 20 mm
Communication frequency	up to 12.5MHz	**Power supplying**	VPROG0: 1.6V – 5.5V VPROG1: 3.5V – 13V
Programmable clock out	up to 50 MHz	**PCI signal supported**	3.3V and 5V
Digital lines	5	**Logging**	Via on-board timekeeper and calendar for time-stamped log files
Analog Lines	2	**LEDs**	Operation status LED for each channel
Host Interface	CompactPCI 2.0 R3.0 compliant, PXI fully compatible	**Dynamic memory**	512 bytes for each channel
ISP connector	D-SUB 50 pins	**Static Memory**	uSD card

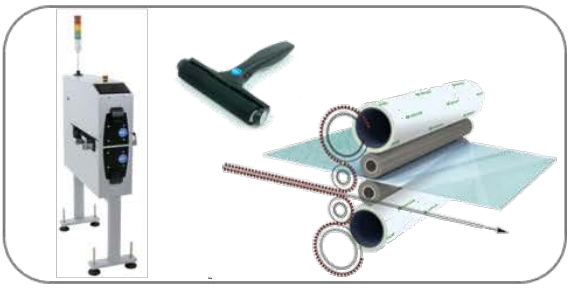
Our Product Portfolio

Feeding Technology

Label Feeder, Labels and Marking Solutions

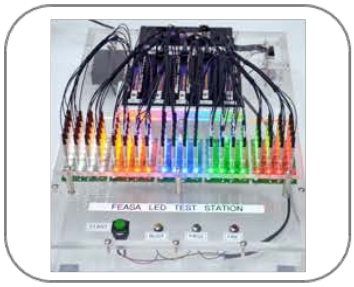


Special Applications



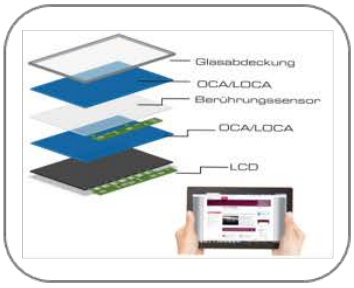
Bare Board Cleaning

In-System Programming

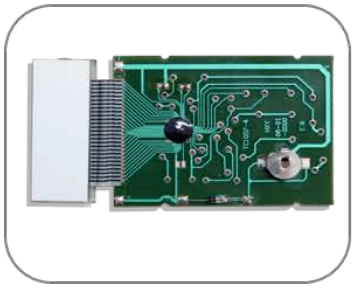


LED Analysis

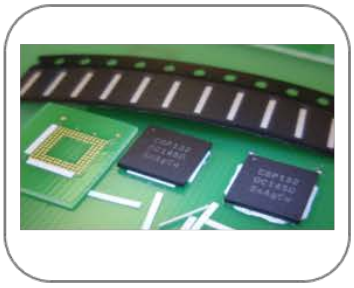
Reflow Inline Camera



Optical Bonding



Thermal Bonding



Place-N-Bond

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