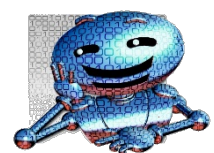


# FlashRunner FRPXIA3 In-System Programmer



**FLASHRUNNER**  
In System Programming Design your needs



# Fast and secure

## Overview

FlashRunner FRPXIA3 is the brand new PXI module, for Gang In System Programming.

FlashRunner PXI is based on FlashRunner technology, the fast and reliable programming system for Flash-based microcontroller and serial memories.

## Features

- First in the world programming solution for PXI system
- Fully hardware and software ATE integration
- Multi-target parallel programming channels
- Powered by Flashrunner technology, the fastest and most reliable programming system.

## This is Flashrunner FRPXIA3

- CompactPCI 2.0 compliant, PXI fully compatible
- Integrated in only one slot of your PXI chassis
- 3 parallel in system programming channels. For each channel:
  - Five digital I/O lines
  - Two digital I/O or analog output lines
  - Two programmable output voltages
  - One programmable clock output
- Fast programming algorithms developed to reach the memory technology speed limit of the target device
- Supports most ISP protocols (BDM, JTAG, SPI, I2C, MON, ICC, SCI, etc.);
- Flexible, fully configurable
- Designed for performance and reliability

## Why PXI?

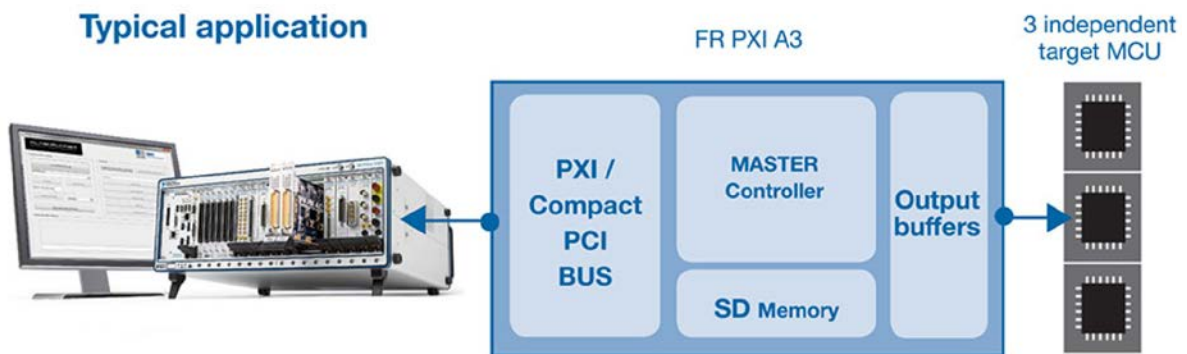
Engineers around the world have made the switch to PXI to realize faster test execution times, improved software development productivity, faster throughput, and increased scalability, which dramatically reduce their overall system costs.

Now, for the very first time, they have the opportunity to integrate the In-System Programming (ISP) function in their PXI systems.

FRPXIA3 is now the ideal solution for the Automatic Test Equipment (ATE) in production environment.

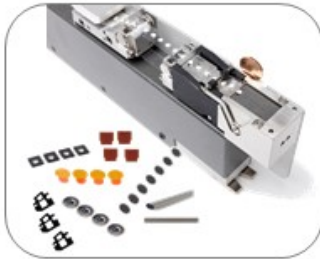
## FlashRunner FRPXIA3 Software Features

- DLL, LabVIEW libraries and examples for easy and fast software integration
- ASCII-Based commands
- Optional Data Protection System to make the contents of the binary file to be programmed to the target device not readable (and not duplicable) by non-authorized people
- Log files
- Erase, blank check, program, read, verify, oscillator trimming, etc.
- FlashRunner's open architecture makes its firmware easily upgradable to support both new devices and new features

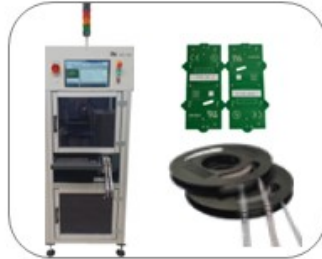


<b>Programming sites</b>	3 independent, parallel	<b>Dimensions</b>	w/o front panel 160 x 100 mm
<b>Protocols</b>	UART, JTAG, SPI, I2C, BDM, SWD etc...	<b>Front Panel Dimension:</b>	128 x 20 mm
<b>Communication frequency</b>	up to 12.5MHz	<b>Power supplying</b>	VPROG0: 1.6V – 5.5V VPROG1: 3.5V – 13V
<b>Programmable clock out</b>	up to 50 MHz	<b>PCI signal supported</b>	3.3V and 5V
<b>Digital lines</b>	5	<b>Logging</b>	Via on-board timekeeper and calendar for time-stamped log files
<b>Analog Lines</b>	2	<b>LEDs</b>	Operation status LED for each channel
<b>Host Interface</b>	CompactPCI 2.0 R3.0 compliant, PXI fully compatible	<b>Dynamic memory</b>	512 bytes for each channel
<b>ISP connector</b>	D-SUB 50 pins	<b>Static Memory</b>	uSD card

## Our Product Portfolio



Label Feeder, also for special parts



Label/ Laser Marker and labels



Tape Feeder and accessories



Tray Handler / Reject Parts Conveyor



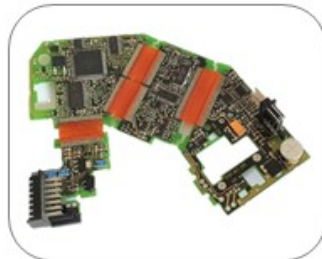
Manual Taping Systems



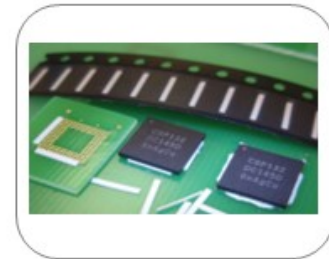
Automatic Packing Systems



Optical Bonding



Thermal Bonding



Place-N-Bond Underfilm



Univers. In-System Programmiers



Reflow Inline Video Camera



Depaneling Systems